



PATENT NUMBER

U.S. UTILITY Patent Application

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O.I.P.E.	PATENT DATE		***
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EXAMINER SUBCLASS ART UNIT CONT/PRIOR | CLASS APPLICATION NO. 257435 2815 400 D 09/651385

Sanjay Dabral Krishna Seshan

Diode and transistor design for high speed I/O

PTO-2040 12/99

		ISSUING C	CLASSIFICATION		
ORIGINA		CROSS REFERENCE(S)			
CLASS SUBCLASS		CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)			
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INTERNATIONAL	CLASSIFICATION				
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	DRAWINGS			CLAIMS ALLOWED	
TERMINAL				OLAIMO ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
The term of this patent				NOTICE OF ALI	LOWANCE MAILED
subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)		
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.				ISS	UE FEE
				Amount Due	Date Paid
	(Primary	Examiner)	(Date)		<u> </u>
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